

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Application No.: 10/042,548  
Filing Date: January 9, 2002  
Applicant: Joseph Sheredy  
Group Art Unit: 2123  
Examiner: Pierre Louis, Andre  
Title: METHOD AND APPARATUS FOR TESTING A SYSTEM  
ON A CHIP (SOC) INTEGRATED CIRCUIT  
COMPRISING A HARD DISK CONTROLLER AND A  
READ CHANNEL  
Attorney Docket: MP0092

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Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

**AMENDMENT AND PETITION FOR EXTENSION OF TIME**

Sir:

Applicant hereby petitions under the provisions of 37 C.F.R. § 1.136(a) for an extension of time in which to respond to the outstanding Office Action and includes a fee as set forth in 37 C.F.R. § 1.17(a) with this response for such extension of time. In response to the Office Action mailed August 4, 2005, please amend the application as follows and consider the remarks set forth below.

**Amendments to the Abstract** begin on page 3 of this paper.

**Amendments to the Claims** begin on page 4 of this paper.

**Remarks** begin on page 13 of this paper.